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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: TOD PAULUS ET AL.

Filed: FEBRUARY 12, 2002

For: NOTCH FILTER FOR DC OFFSET REDUCTION IN RADIO-FREQUENCY APPARATUS AND ASSOCIATED METHODS

Serial No.: 10/075,099

Group Art Unit: 2684

Examiner: LEE, J.

Arty Docket No.: SILA:097

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on the date below:

6.7.05

Mandy Jones

Date Name

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313

**SUBMISSION OF INFORMATION DISCLOSURE STATEMENT PURSUANT TO
OFFICE ACTION DATED DECEMBER 1, 2004**

Sir:

In response to the Office Action dated December 1, 2004, Applicant hereby submits a substitute Information Disclosure Statement including copies of references A1-A51, B1-B6, and C1-91.

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Respectfully submitted,



Maximilian R. Peterson
Registration No. 46,469
Attorney for Applicant

O'KEEFE, EGAN & PETERMAN, L.L.P.
1101 Capital of Texas Highway South
Building C, Suite 200
Austin, Texas 78746
512-347-1611
512-347-1615 (Fax)



Received in the U.S. Patent and Trademark Office:
In re Application of: Tod Paulus et al.
Notch Filter For DC Offset Reduction In Radio-Frequency
Apparatus and Associated Methods
Serial No. 10/075,099; Filed: 2/12/02

Enclosed herewith:

1. Information Disclosure Statement
2. PTO Form 1449
3. Cited references A1-A48; B1-B6 and C1-C91
4. Our return postcard.

SILICON LABORATORIES, INC. - SILA:097
Mailed: 11-25-02
MRP/mb

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In re Application of: **TOD PAULUS ET AL.**

Filed: **FEBRUARY 12, 2002**

For: **NOTCH FILTER FOR DC OFFSET REDUCTION IN RADIO-FREQUENCY APPARATUS AND ASSOCIATED METHODS**

Serial No.: **10/079,099**

Group Art Unit: **2681**

Examiner: **UNKNOWN**

Atty Dkt: **SILA:097**

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

11-25-02

Marty Bowles

Date

Name

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

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Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

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The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in



accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SILA:097.

Per 37 CFR 1.98(d), no copies of references A1-A48, B1-B6 and C1-91 have been provided, as copies of these references have been previously submitted to the Office in one or more of co-pending U.S. Patent Application Serial Nos. 09/821,340 filed on March 29, 2001, which is entitled "Digital Interface In Radio-Frequency Apparatus And Associated Methods" and 09/821,342 filed on March 29, 2001, which is entitled "Partitioned Radio-Frequency Apparatus And Associated Methods" and which is relied upon by the present application for an earlier effective filing date under 35 U.S.C. Section 120.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

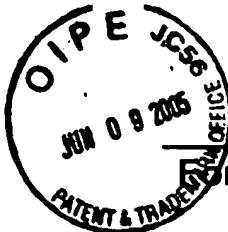
Respectfully submitted,

A handwritten signature in black ink that appears to read "M/R Peterson".

Maximilian R. Peterson
Reg. No. 46,469
Attorney for Applicant

O'KEEFE, EGAN & PETERMAN, LLP
1101 Capital of Texas Highway South
Building C, Suite 200
Austin, Texas 78746
(512) 347-1611
FAX: (512) 347-1615

Enclosures



Form PTO-1449 (modified)

List of Patents and Publications for Applicant's

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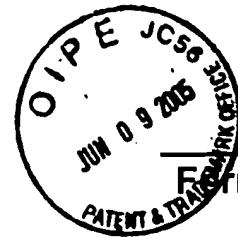
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 2681

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| | B6 | WO 99/22456 | 5/6/99 | Grenabo | | | 10/27/98 |

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| | C1 | Stephen Jantzi et al., "Quadrature Bandpass $\Delta\Sigma$ Modulation for Digital Radio," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 1935-1950. |
| | C2 | Stephen Jantzi et al, "A Complex Bandpass $\Delta\Sigma$ Converter For Digital Radio," ISCAS, May/June 1994, pp. 453-456. |
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Examiner:

Date Considered:

EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Form PTO-1449 (modified)

List of Patents and Publications for Applicant's

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Atty. Docket No.
SILA:097Serial No.
10/075,099Applicants
TOD PAULUS ET AL.Filing Date:
2/12/02Group:
2681U.S. Patent Documents
See Pages 1-3Foreign Patent Documents
See Pages 3Other Art
See Pages 3-10

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

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| | C61 | Schreier et al., "Decimation For Bandpass Sigma-Delta Analog-To-Digital Conversion," IEEE, 1990, pp. 1801-1804. |
| | C62 | Shoaei et al., "Optimal (Bandpass) Continuous-Time $\Delta\Sigma$ Modulator," pp. 489-492. |
| | C63 | Schreier et al., "Bandpass Sigma-Delta Modulation," Electronics Letters, Vol. 25, no. 23, November 9, 1989, pp. 1560-1561. |
| | C64 | Jantzi et al., "Bandpass Sigma-Delta Analog-To-Digital Conversion," IEEE Transactions On Circuits And Systems, Vol. 38, No. 11, November 1991, pp. 1406-1409. |
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| | C71 | Rudell, et al., "Second Generation Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 9 (June 1996) |
| | C72 | Cho, et al., "Multi-Standard Monolithic CMOS RF Transceiver," University of California, Berkeley, Slides 1 through 26 (June 1996) |
| | C73 | Copending U.S. Patent Application Serial No. 09/821,342, filed March 29, 2001, "Partitioned Radio-Frequency Apparatus And Associated Method" (SILA:072) |
| | C74 | Copending U.S. Patent Application Serial No. 09/821,340, filed March 29, 2001, "Digital Interface In Radio-Frequency Apparatus And Associated Methods" (SILA:073) |

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|----------------|--------------|--|
| | C75 | Copending U.S. Patent Application Serial No. 10/075,094, filed February 13, 2002, "Radio-Frequency Communication Apparatus And Associated Methods" (Sila:074) |
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| | C77 | Copending U.S. Patent Application Serial No. 10/075,122, filed February 12, 2002, "Digital Architecture For Radio-Frequency Apparatus And Associated Methods" (Sila:078) |
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| | C80 | Copending U.S. Patent Application Serial No. 10/074,591, filed February 13, 2002, "Apparatus For Generating Multiple Radio Frequencies In Communication Circuitry And Associated Methods" (Sila:096) |
| | C81 | Copending U.S. Patent Application Serial No. 10/075,099, filed February 12, 2002, "Notch Filter For DC Offset Reduction In Radio-Frequency Apparatus And Associated Methods" (Sila:097) |
| | C82 | Copending U.S. Patent Application Serial No. 10/074,676, filed February 12, 2002, "DC Offset Reduction In Radio-Frequency Apparatus And Associated Methods" (Sila:098) |
| | C83 | Copending U.S. Patent Application Serial No. 10/079,058, filed February 19, 2002, "Apparatus And Methods For Output Buffer Circuitry With Constant Output Power In Radio-Frequency Circuitry" (Sila:099) |
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| | C87 | Motorola Communications Semiconductor Product Division, "A 1.9 GHz Chipset For PCS Applications," Microwave Journal, No. 6, June 1995, 3 pgs. |
| | C88 | Search Report for PCT/US02/00896; October 4, 2002; 7 pgs. |
| | C89 | Copending U.S. Patent Application Serial No. 09/708,339, filed November 8, 2000, "Method And Apparatus For Operating A PLL With A Phase Detector/Sample Hold Circuit For Synthesizing High-Frequency Signals For Wireless Communications" (Sila:035C1) |
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| | C91 | Search Report for PCT/US02/00895; November 11, 2002; 6 pgs. |

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U.S. Patent Documents

| Exam. Init. | Ref. Des. | Document Number | Date | Name | Class | Sub Class | Filing Date if App. |
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| | A49 | 6,539,066 | 3/25/03 | Heinen | | | 11/10/99 |
| | A50 | 6,343,207 | 1/29/02 | Hessel et al. | | | 11/3/98 |
| | A51 | 6,002,925 | 12/14/99 | Vu et al. | | | 3/24/97 |

Foreign Patent Documents

| Exam. Init. | Ref. Des. | Document Number | Date | Name | Class | Sub Class | Filing Date if App. |
|-------------|-----------|-----------------|------|------|-------|-----------|---------------------|
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Received in the U.S. Patent and Trademark Office:
In re Application of: Tod Paulus et al.
**Notch Filter For DC Offset Reduction In Radio-Frequency
Apparatus and Associated Methods**
Serial No. 10/075,099; Filed: 2/12/02

Enclosed herewith:

1. Supplemental Information Disclosure Statement
2. PTO Form 1449
3. Cited references A49-A51 (copies not provided)
4. Our return postcard.

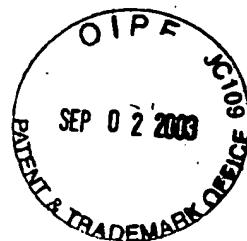
SILICON LABORATORIES, INC. – SILA:097
Mailed: 8/26/03
MRP/mw

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In re Application of: Tod Paulus et al.
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Mailed: 8/26/03
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: TOD PAULUS ET AL.

Filed: FEBRUARY 12, 2002

For: NOTCH FILTER FOR DC OFFSET REDUCTION IN RADIO-FREQUENCY APPARATUS AND ASSOCIATED METHODS

Serial No.: 10/075,099

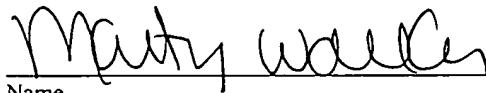
Group Art Unit: 2681

Examiner: UNKNOWN

Atty Dkt: SILA:097

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313 on the date below:

8-26-03



Name

Date

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

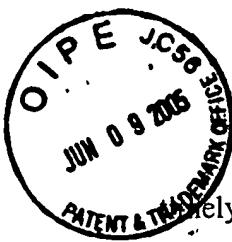
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Supplemental Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be



solely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Supplemental Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SILA:097.

Per 37 CFR 1.98(d), no copies of references A49-A51 have been provided, as copies of these references are of record or being submitted in one or more of co-pending U.S. Patent Application Serial Nos. 09/821,340 filed on March 29, 2001, which is entitled "Digital Interface In Radio-Frequency Apparatus And Associated Methods" and 09/821,342 filed on March 29, 2001, which is entitled "Partitioned Radio-Frequency Apparatus And Associated Methods" and one of which is relied upon by the present application for an earlier effective filing date under 35 U.S.C. Section 120.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,



Maximilian R. Peterson
Reg. No. 46,469
Attorney for Applicant

O'KEEFE, EGAN & PETERMAN, LLP
1101 Capital of Texas Highway South
Building C, Suite 200
Austin, Texas 78746
(512) 347-1611
FAX: (512) 347-1615

Enclosures